

(19)



JAPANESE PATENT OFFICE

PATENT ABSTRACTS OF JAPAN

(11) Publication number: **2002091342 A**

(43) Date of publication of application: **27.03.02**

(51) Int. Cl.

G09F 9/30
G02F 1/1368
H01L 21/768
H01L 29/786
// G02F 1/1333

(21) Application number: **2000319948**

(22) Date of filing: **19.10.00**

(30) Priority: **10.07.00 JP 2000208614**

(71) Applicant: **TOSHIBA CORP**

(72) Inventor: **KAWANO HIDEO**
KITAHATA MOTOYUKI

(54) **MATRIX ARRAY BOARD**

(57) Abstract:

PROBLEM TO BE SOLVED: To provide a matrix array board which is used for a flat-panel display device or the like, is provide with a tandem repair circuit 6 between an extension part 51-1 of a pixel electrode 5-1 for forming auxiliary capacitance and its adjacent (prestage) pixel electrode 5-2, lessens reduction in a pixel aperture rate due to the installation of the circuit 6 and prevents the generation of a short circuit between both pixel electrodes 5-1 and 5-2 due to an etching residue.

SOLUTION: A contact hole 45 for connecting the pixel electrode extension part 51-1 to one connection electrode 35 of the tandem repair circuit 6 is formed in an area on the scanning line 11.

COPYRIGHT: (C)2002,JPO

